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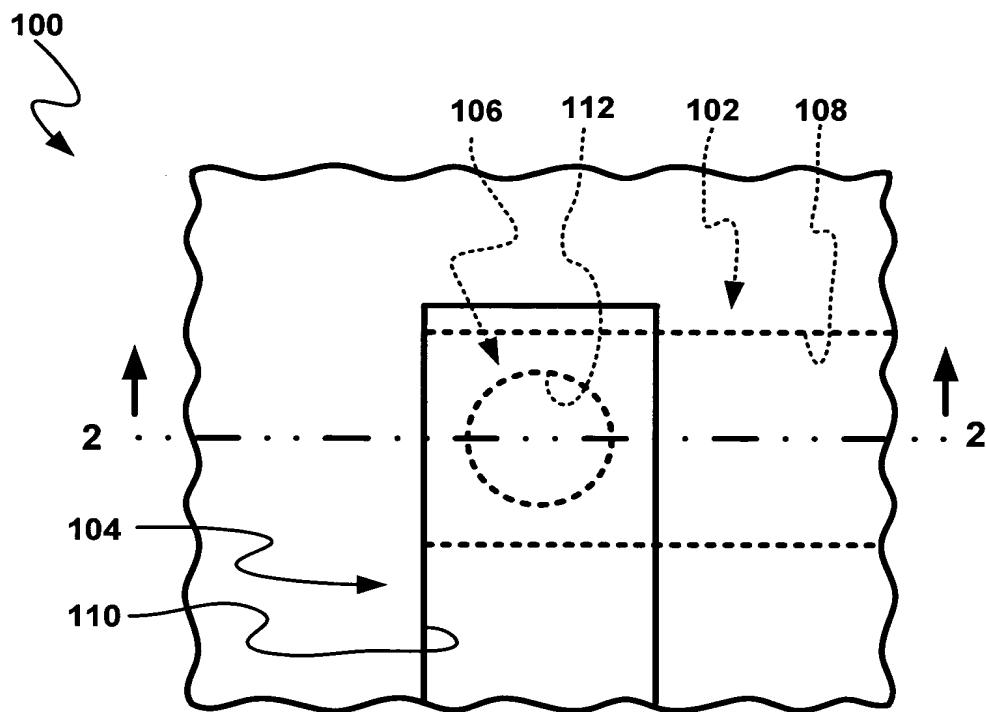


FIG. 1 (PRIOR ART)

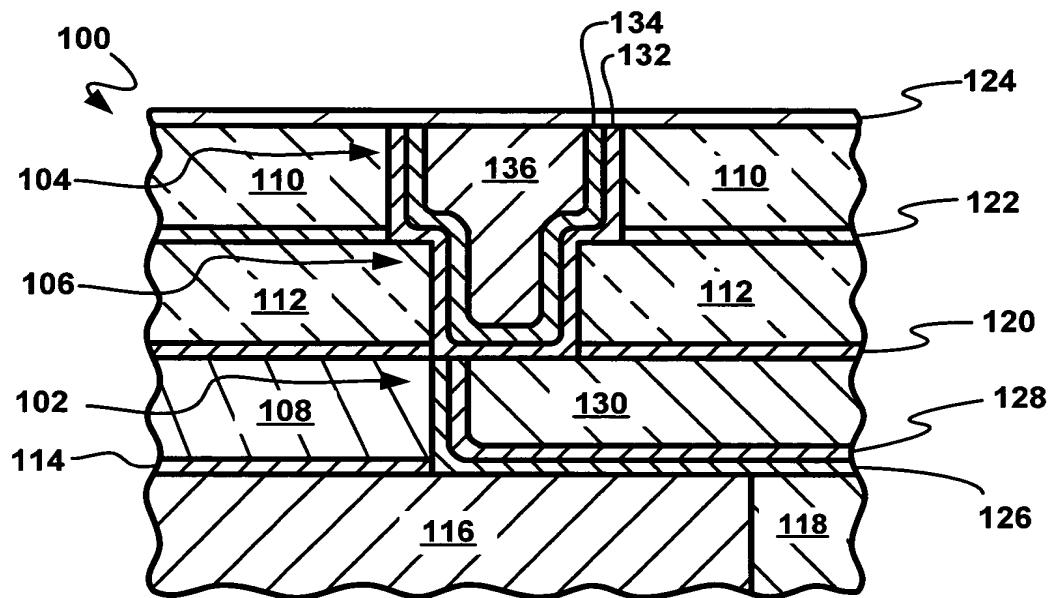


FIG. 2 (PRIOR ART)

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Title: AN INTEGRATED CIRCUIT WITH  
DIELECTRIC DIFFUSION BARRIER LAYER  
FORMED BETWEEN INTERCONNECTS AND  
INTERLAYER DIELECTRIC LAYERS  
Inventors: Steven C. Avanzino, et al.  
Docket No.: F0361.C1.D1  
Contact: Mikio Ishimaru (408) 738-0592

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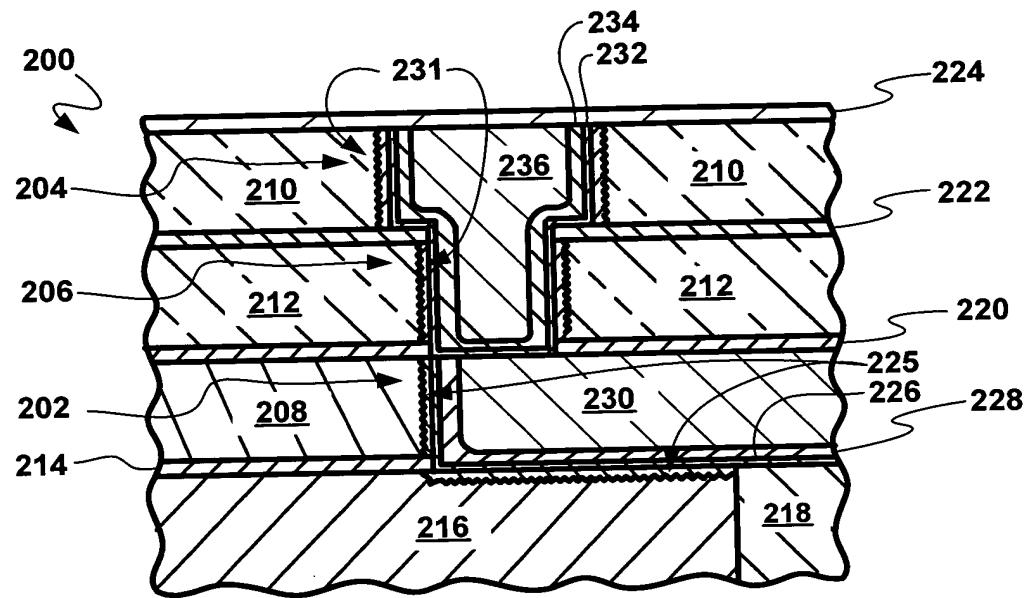


FIG. 3